

Hit List

[First Hit](#)[Clear](#)[Generate Collection](#)[Print](#)[Fwd Refs](#)[Bkwd Refs](#)[Generate OACS](#)

Search Results - Record(s) 1 through 12 of 12 returned.

☐ 1. Document ID: US 7072922 B2

L2: Entry 1 of 12

File: USPT

Jul 4, 2006

US-PAT-NO: 7072922

DOCUMENT-IDENTIFIER: US 7072922 B2

TITLE: Integrated circuit and process for identifying minimum or maximum input value among plural inputs

DATE-ISSUED: July 4, 2006

PRIOR-PUBLICATION:

DOC-ID

DATE

US 20040117416 A1

June 17, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Andreev; Alexander E.	San Jose	CA		US
Bolotov; Anatoli A.	Cupertino	CA		US
Vikhliantsev; Igor	Sunnyvale	CA		US

US-CL-CURRENT: 708/207

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Drawings
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	-----	----------

☐ 2. Document ID: US 7057546 B1

L2: Entry 2 of 12

File: USPT

Jun 6, 2006

US-PAT-NO: 7057546

DOCUMENT-IDENTIFIER: US 7057546 B1

TITLE: Binary priority encoder

DATE-ISSUED: June 6, 2006

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Secatch; Stacey	Niwot	CO		US
Ogden; James E.	San Jose	CA		US

US-CL-CURRENT: 341/160; 341/159

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KM/C	Draw. De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	----------

☐ 3. Document ID: US 6772187 B1

L2: Entry 3 of 12

File: USPT

Aug 3, 2004

US-PAT-NO: 6772187DOCUMENT-IDENTIFIER: US 6772187 B1

TITLE: Parallel greater than analysis method and apparatus

DATE-ISSUED: August 3, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ott; Michael L.	Pleasanton	CA		
Chng; Choon Ping	Sunnyvale	CA		
Tzeng; Tzungren Allen	San Jose	CA		

US-CL-CURRENT: 708/700; 708/160

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KM/C	Draw. De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	----------

☐ 4. Document ID: US 6769005 B1

L2: Entry 4 of 12

File: USPT

Jul 27, 2004

US-PAT-NO: 6769005

DOCUMENT-IDENTIFIER: US 6769005 B1

TITLE: Method and apparatus for priority resolution

DATE-ISSUED: July 27, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ott; Michael	Pleasanton	CA		

US-CL-CURRENT: 708/207

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KM/C	Draw. De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	----------

☐ 5. Document ID: US 6748405 B2

L2: Entry 5 of 12

File: USPT

Jun 8, 2004

US-PAT-NO: 6748405

DOCUMENT-IDENTIFIER: US 6748405 B2

TITLE: Method and circuits for performing the quick search of the minimum/maximum value among a set of numbers

DATE-ISSUED: June 8, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
de Tremiolles; Ghislain Imbert	Saint-Paul			FR
Louis; Didier	Fontainebleau			FR
Tannhof; Pascal	Fontainebleau			FR

US-CL-CURRENT: 708/207

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMIC	Draw. De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	----------

☐ 6. Document ID: US 6734984 B2

L2: Entry 6 of 12

File: USPT

May 11, 2004

US-PAT-NO: 6734984

DOCUMENT-IDENTIFIER: US 6734984 B2

TITLE: System having an arithmetic-logic circuit for determining the maximum or minimum of a plurality of codes

DATE-ISSUED: May 11, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Prenn; M. Therese	Star	ID		
Lee; Chang H.	Seattle	WA		

US-CL-CURRENT: 358/1.12; 358/1.17, 710/260, 710/263

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMIC	Draw. De
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	----------

☐ 7. Document ID: US 6356354 B1

L2: Entry 7 of 12

File: USPT

Mar 12, 2002

US-PAT-NO: 6356354

DOCUMENT-IDENTIFIER: US 6356354 B1

**** See image for Certificate of Correction ****

TITLE: System having an arithmetic-logic circuit for determining the maximum or minimum of a plurality of codes

DATE-ISSUED: March 12, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Prenn; M. Therese	Star	ID		
Lee; Chang H.	Seattle	WA		

US-CL-CURRENT: 358/1.14; 358/1.15, 358/437

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	K/MC	Draw. D
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	---------

☐ 8. Document ID: US 5721809 A

L2: Entry 8 of 12

File: USPT

Feb 24, 1998

US-PAT-NO: 5721809DOCUMENT-IDENTIFIER: US 5721809 A

TITLE: Maximum value selector

DATE-ISSUED: February 24, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Park; Soung Hwi	Pusan-si			KR

US-CL-CURRENT: 706/4; 340/146.2, 708/207

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	K/MC	Draw. D
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	---------

☐ 9. Document ID: US 5515306 A

L2: Entry 9 of 12

File: USPT

May 7, 1996

US-PAT-NO: 5515306DOCUMENT-IDENTIFIER: US 5515306 A

TITLE: Processing system and method for minimum/maximum number determination

DATE-ISSUED: May 7, 1996

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Blaner; Bartholomew	Underhill Center	VT		
Stroupbauer; Dennis R.	S. Burlington	VT		

US-CL-CURRENT: 708/207; 340/146.2

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	K/MC	Draw. D
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	------	---------

☐ 10. Document ID: US 5148373 A

L2: Entry 10 of 12

File: USPT

Sep 15, 1992

US-PAT-NO: 5148373

DOCUMENT-IDENTIFIER: US 5148373 A

TITLE: Method and an arrangement for accurate digital determination of the time or phase position of a signal pulse train

DATE-ISSUED: September 15, 1992

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Dent; Paul W.	Stehag			SE

US-CL-CURRENT: 702/72; 370/201

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. D
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	-----	---------

☐ 11. Document ID: US 5122979 A

L2: Entry 11 of 12

File: USPT

Jun 16, 1992

US-PAT-NO: 5122979

DOCUMENT-IDENTIFIER: US 5122979 A

TITLE: Method and a digital electronic device for the evaluation of an extremum of a set of binary encoded data words

DATE-ISSUED: June 16, 1992

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Culverhouse; Philip F.	Devon			GB2

US-CL-CURRENT: 708/207; 340/146.2

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. D
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	-----	---------

☐ 12. Document ID: US 4998219 A

L2: Entry 12 of 12

File: USPT

Mar 5, 1991

US-PAT-NO: 4998219DOCUMENT-IDENTIFIER: US 4998219 A**** See image for Certificate of Correction ****

TITLE: Method and apparatus for determining the greatest value of a binary number and for minimizing any uncertainty associated with the determination

DATE-ISSUED: March 5, 1991

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Frauenglass; Richard M.	Holbrook	NY		

US-CL-CURRENT: 708/207

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw. D.
------	-------	----------	-------	--------	----------------	------	-----------	-----------	-------------	--------	-----	----------

[Clear](#)[Generate Collection](#)[Print](#)[Fwd Refs](#)[Bkwd Refs](#)[Generate OACS](#)

Terms

Documents

L1 and binary

12

Display Format: [CIT](#)[Change Format](#)[Previous Page](#)[Next Page](#)[Go to Doc#](#)